

ABSTRACT OF THE DISCLOSURE

An interpreter invocation mechanism for switching between execution of native instruction words (ARM, Thumb) to interpreted instruction words (Photon) uses a subroutine call instruction (14) to start execution of the interpreter. The return address of the subroutine call instruction (14) is used as an address pointer to the start of the interpreted code (16). The interpreted code may terminate with an Exit instruction whereupon normal native code execution resumes using the instruction at the immediately following memory address or alternatively with a Return instruction that recovers a return address previously stored to stack memory.

[Figure 3]

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